Refine Search

Search Results -

Terms	Documents
(deposit\$3 adj chamber) and (configur\$3) and (wafers) and (measur\$3 adj3 thickness adj3 wafer) and (pattern)	8

US Pre-Grant Publication Full-Text Database
US Patents Full-Text Database
US OCR Full-Text Database
EPO Abstracts Database
JPO Abstracts Database
Derwent World Patents Index
IBM Technical Disclosure Bulletins

Search:

Database:

L16			Refine Search
Recall	ext 😂	Clear	Interrupt

Search History

DATE: Thursday, October 07, 2004 Printable Copy Create Case

Set Name side by side	Query	Hit Count	Set Name result set
DB=B	PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD; PLUR=NO; OP=OR		
<u>L16</u>	(deposit\$3 adj chamber) and (configur\$3) and (wafers) and (measur\$3 adj3 thickness adj3 wafer) and (pattern)	8	<u>L16</u>
<u>L15</u>	(deposit\$3 adj chamber) and (configur\$3 adj3 parameter) and (wafers)	24	<u>L15</u>
<u>L14</u>	(deposit\$3 adj chamber) and (configur\$3 adj3 parameter) and (wafers) and (pattern adj5 thickness)	0	<u>L14</u>
<u>L13</u>	(deposit\$3 adj chamber) and (configur\$3 adj3 parameter) and (wafers) and (wafer adj3 thickness)	0	<u>L13</u>
<u>L12</u>	(deposit\$3 adj chamber) and (configur\$3 adj3 parameter) and (wafers) and (wafer adj3 thickness) and (pattern)	0	<u>L12</u>
<u>L11</u>	(deposit\$3 adj chamber) and (configur\$3 adj3 parameter) and (wafers) and (wafer adj3 tickness) and (pattern)	0	<u>L11</u>
<u>L10</u>	(deposit\$3 adj chamber) and (configur\$3 adj3 parameter) and (wafers) and (measur\$3 adj tickness) and (pattern)	0	<u>L10</u>

<u>L9</u>	(deposit\$3 adj chamber) and (configur\$3 adj3 parameter) and (wafers) and (measur\$3 adj tickness) and (deposition adj pattern)	0	<u>L9</u>
<u>L8</u>	(deposit\$3 adj chamber) and (configur\$3 adj3 parameter) and (wafers) and (measur\$3 adj thickness) and (pattern)	23	<u>L8</u>
<u>L7</u>	(deposit\$3 adj chamber) and (configur\$3 adj3 parameter) and (wafers) and (thickness) and (pattern)	24	<u>L7</u>
<u>L6</u>	(deposit\$3 adj chamber) and (configur\$3 adj3 parameter) and (wafers) and (thickness) and (pattern)	24	<u>L6</u>
<u>L5</u>	(deposit\$3 adj chamber) and (configur\$3 adj parameter) and (wafers) and (thickness) and (pattern)	0	<u>L5</u>
<u>L4</u>	(deposit\$3 adj chamber) and (configur\$3 adj parameter) and (wafers) and (measur\$3 adj thickness) and (pattern)	0	<u>L4</u>
<u>L3</u>	(deposit\$3 adj chamber) and (configur\$3 adj parameter) and (wafers) and (measur\$3 adj thickness) and (deposition adj pattern)	0	<u>L3</u>
<u>L2</u>	(deposit\$3 adj chamber) and (configur\$3 adj parameter) and (wafers) and (measur\$3 adj tickness)	0	<u>L2</u>
<u>L1</u>	(deposit\$3 adj chamber) and (configur\$3 adj parameter) and (wafers) and (measur\$3 adj tickness) and (deposition adj pattern)	0	<u>L1</u>

END OF SEARCH HISTORY